

Lamp driver using solar cells

FIELD OF THE INVENTION

The present invention relates in general to a lamp driver capable of driving a gas discharge lamp, specifically a high-frequency gas discharge lamp, more specifically a high-frequency TL lamp. More particularly, the present invention relates to a lamp driver 5 capable of being powered by a solar cell.

BACKGROUND OF THE INVENTION

Gas discharge lamps are commonly known. In general, they comprise a light transmitting vessel enclosing a discharge chamber in a gastight manner, an ionizable filling, 10 and a pair of electrodes located opposite each other in the discharge chamber, each electrode being connected to an associated current conductor which extends from the discharge chamber through the lamp vessel to the exterior. During operation, a voltage is applied over said electrodes, and a gas discharge occurs between said electrodes causing a lamp current to flow between the electrodes.

15 A discharge lamp is typically driven by an electronic ballast or driver supplying an alternating current. Lamp drivers and solar power devices have been developed separately. Typically, a lamp driver is developed for being powered from mains, and comprises:

- an input for receiving AC mains;
- 20 - a rectifier for rectifying the mains voltage to a direct voltage;
- a DC/DC up converter for receiving the rectified voltage and for converting this voltage to a higher voltage and usually also for performing a power factor correction for the mains current;
- a half-bridge inverter for inverting said high direct voltage to AC of the proper amplitude 25 (lamp current).

The inverter behaves as a current source. Typically, the inverter operates at a frequency in the order of about 40 - 50 kHz. Therefore, in principle, the lamp is operated at a lamp current having constant magnitude but regularly changing its direction within a very brief time (commutating periods) in a symmetric way, i.e. an electrode is operated as a

cathode during 50% of each current period and is operated as anode during the other 50% of each current period.

On the other hand, a typical solar voltage source, capable of receiving solar energy and generating an output voltage, comprises:

- 5 - at least one photo-voltaic cell, capable of receiving light and generating an output current;
- a boost converter, receiving the output current from the photo-voltaic cell and generating an output voltage.

Typically, the electric energy provided by a solar voltage source is used to charge a battery or other type of accumulator (e.g. the mains grid), and the battery is used to 10 act as power source for an electric apparatus.

In a lamp driver, it is desirable to be able to use solar energy directly for driving a discharge lamp. Therefore, a solar energy powered lamp driver has been developed, combining elements of a typical solar voltage source and elements of a typical discharge lamp driver. Fig. 1 is a block diagram schematically showing the general design of a prior art 15 solar energy powered lamp driver 1, which comprises three stages, i.e. a boost converter 10, a pulse width modulated inverter 20, and a half-bridge inverter 30.

The boost converter 10 comprises at least one photo-voltaic cell 11, an inductor 12 having one terminal coupled to an output of the photo-voltaic cell 11, a controllable switch 13 coupled between a second inductor 12 terminal and a reference 20 voltage (mass), and a rectifying element 14 having one terminal coupled to a node A between said inductor 12 and said switch 13 and having another terminal coupled to an output 15 of the boost converter 10.

The pulse width modulated inverter 20 comprises a first branch of two controllable switches 21 and 22 coupled in series between said boost converter output 15 and 25 said reference voltage (mass); a node between said two controllable switches 21 and 22 is indicated at B. The pulse width modulated inverter 20 comprises a second branch of two controllable switches 23 and 24 coupled in series between said boost converter output 15 and said reference voltage (mass); a node between said two controllable switches 23 and 24 is indicated at C.

30 The half-bridge inverter 30 comprises a third branch of two controllable switches 31 and 32 coupled in series between said boost converter output 15 and said reference voltage (mass); a node between said two controllable switches 31 and 32 is indicated at D. The half-bridge inverter 30 comprises a fourth branch of two capacitors 33 and 34 coupled in series between said boost converter output 15 and said reference voltage

(mass); a node between said two capacitors 33 and 34 is indicated at E. The half-bridge inverter 30 further comprises an arrangement of a lamp L, a capacitor 35 and an inductor 36 coupled in series between said two nodes D and E of the converter 30.

The pulse width modulated inverter 20 further comprises an inductor 25 which 5 is coupled, in series with an AC mains input/output 36, between said two nodes B and C of the inverter 20. If there is insufficient solar energy available, energy is received from the AC mains for powering the lamp L. If there is more solar energy available than consumed by the lamp L, the surplus is fed back to the mains.

10 The said switches, which are typically implemented as MOSFETs, are driven by a switch controller not shown in Fig. 1 for sake of simplicity.

Such prior art solar energy powered lamp driver 1 has several disadvantages. The device has a large number of components, i.e. at least seven switches and at least three inductors. Further, there is a considerable loss of energy from photo-voltaic cell 11 to lamp L 15 because of the three conversion stages.

An important objective of the present invention is to reduce these disadvantages. More particularly, it is an objective of the present invention to provide a solar energy powered lamp driver with a reduced component count and an improved efficiency.

20 SUMMARY OF THE INVENTION

According to an important aspect of the present invention, the output of the boost converter is connected directly to a node between the two controllable switches of the half-bridge inverter/converter, and the mains input/output is connected in parallel to the series arrangement of lamp and series inductor, thus allowing the pulse width modulated 25 inverter stage to be omitted entirely while allowing the performance of the driver to be maintained.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other aspects, features and advantages of the present invention will 30 be further explained by the following description with reference to the drawings, in which same reference numerals indicate same or similar parts, and in which:

Fig. 1 is a block diagram schematically showing the general design of a prior art solar energy powered lamp driver;

Fig. 2 is a block diagram schematically showing the general design of a first embodiment of a solar energy powered lamp driver according to the present invention;

Fig. 3 is a block diagram schematically showing the general design of a second embodiment of a solar energy powered lamp driver according to the present invention.

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DETAILED DESCRIPTION OF THE INVENTION

Fig. 2 schematically shows a block diagram of a first embodiment of a solar energy powered lamp driver 100 according to the present invention, comprising a boost stage 10 110 and a half-bridge inverter 130.

The boost converter 110 comprises at least one photo-voltaic cell 111, an inductor 112 having one terminal coupled to an output of the photo-voltaic cell 111 and having its other terminal coupled to a first terminal (in this case an anode) of a rectifying element 114 (shown as a diode) having one terminal (in this case a cathode) coupled to an 15 output 115 of the boost converter 110.

The half-bridge inverter 130 comprises a first branch of two controllable switches 131 and 132 coupled in series between a first reference voltage V1 and a second reference voltage (mass); a node between said two controllable switches 131 and 132 is indicated at D. The half-bridge inverter 130 comprises a second branch of two buffer 20 capacitors 133 and 134 coupled in series between said first reference voltage V1 and said second reference voltage (mass); a node between said two capacitors 133 and 134 is indicated at E. The half-bridge inverter 130 further comprises an output branch DE connected between said two nodes D and E of the converter 130. In the embodiment shown, the output branch DE comprises an arrangement of a lamp L, a capacitor 135 and an inductor 136 coupled in 25 series between said two nodes D and E of the converter 130. In this respect, it is noted that the lamp L actually is not part of the converter 130; the converter 130 comprises a lamp output for receiving a lamp L, but these details, which will be clear to a person skilled in the art, are not illustrated in Fig. 2.

Since the reference voltage V1 is not provided by an external source, but is 30 derived from the mains and/or the solar cell through boost-conversion operation of the two switches 131, 132, the node between first switch 131 and first buffer capacitor 133 will hereinafter be indicated as "first reference node". Likewise, the node between second switch 132 and second buffer capacitor 134 (mass) will hereinafter be indicated as "second reference node".

The half-bridge inverter 130 further comprises an inductor 137 which is coupled, in series with an AC mains input/output 138, between said two nodes D and E of the converter 130, i.e. in parallel to the series arrangement of lamp L, capacitor 135 and inductor 136. If there is insufficient solar energy available, energy is received from the AC mains for 5 powering the lamp L. In this case the switches work as a voltage-doubling boost converter that stores energy in capacitors 133 and 134. At the same time, these switches work as an inverter supplied from these capacitors 133 and 134, delivering energy to lamp 136. The two energy streams can be independently controlled by driving switches 131 and 132 with a mix of PWM and FM (giving the necessary two degrees of freedom). If there is more solar energy 10 available than consumed by the lamp L, the surplus is fed back to the mains 138.

The said switches, which are typically implemented as MOSFETs, are driven by a switch controller 140.

When comparing the first solar energy powered lamp driver 100 according to 15 the present invention with the prior art driver 1 as illustrated in Fig. 1, it will immediately be evident that the driver 100 of the invention has less components, especially less switches: only two switches, i.e. first switch 131 and second switch 132, suffice in this embodiment. First switch 131 performs the double function of upper inverter switches (compare prior art switches 21 and 23), and upper half-bridge switch (compare prior art switch 31). Second 20 switch 132 performs the triple function of boost switch (compare prior art switch 13), lower inverter switches (compare prior art switches 22 and 24), and lower half-bridge switch (compare prior art switch 32).

The switch controller 140 generates control signals for controlling the two switches 131 and 132 to either their conductive or their non-conductive state, taking care that 25 the two switches are not conductive simultaneously, as is commonly known to persons skilled in the art. The switch controller 140 is adapted to drive the two switches with a combination of frequency modulation (FM) and pulse width modulation (PWM). More particularly, the switch controller 140 is adapted to set the switching frequency of the two switches 131 and 132 such as to obtain a certain desired lamp current, and to set the duty 30 cycle of the switches (i.e. the ratio of the ON period of a switch to the entire switching period) such as to obtain a certain desired mains current. Preferably, the switches are controlled to maintain a fixed switching frequency during a mains cycle.

The switching frequency of the switches 131, 132 and the impedance of the decoupling capacitor 135 are chosen such that the lamp L can not be powered directly from

mains 138. Typically, the mains frequency is in the order of 50-60 Hz, and the operating frequency of the switches is selected well above this mains frequency, preferably not lower than 20 kHz. In case the lamp L is a high-frequency TL lamp, an operating frequency in the order of 40-50 kHz is adequate.

5 By selecting a large distance between mains frequency and switch operating frequency, it is relatively easily possible to select a suitable value for the decoupling capacitor 135 so that it has a relatively large impedance for the mains frequency and a relatively low impedance for the switch operating frequency. This will prevent the mains current from flowing directly to the lamp L, while allowing the lamp L to receive high-
10 frequency current from the buffer capacitors. Likewise, it is relatively easily possible to select a suitable value for the inductor 137 in series with the mains 138, such that it has a high impedance in order to prevent high-frequency components from reaching the mains.

15 Fig. 3 schematically shows a second embodiment of a solar energy powered lamp driver 200 according to the present invention, which is identical to the first embodiment 100 except that an additional switch 113 is added to the boost stage 110, connected between on the one hand a node A between the inductor 112 and the rectifying element 114 and on the other hand the second reference node (mass). The function of this additional switch 113 is comparable to the switch 13 in the boost converter 10 of the prior art driver 1.

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When designing a lamp driver, there are three important performance parameters which should ideally be optimized at the same time:
1a- power quality if there is no or insufficient solar energy available (for instance during the night) and the driver consumes energy from the mains; desirably, power quality involves a power factor in the order of 1 and a total harmonic distortion less than 30%;
1b- power quality of the power provided to the mains, in case of a surplus of solar energy (i.e. much sun light and/or little or no lamp power consumption);
2- constant lamp power, i.e. ripple-free and flicker-free light output;
3- maximum power point tracking, i.e. a setting such that always maximum power is taken from the solar cells.

30 In the prior art driver 1 as illustrated in Fig. 1, which comprises three individual converter stages which are driven independently, the setting of the boost converter stage 10 is optimized to fulfil the requirement of the third parameter (maximum power point tracking), the setting of the half-bridge stage 30 is optimized to fulfil the requirement of the

second parameter (ripple-free and flicker-free light output), and the setting of the inverter stage 20 is optimized to fulfil the requirement of the first parameter (power quality).

In the embodiment of Fig. 3, which comprises three switches which can basically be controlled independently, it is possible to optimize all three of said parameters 5 independently. In the embodiment of Fig. 2, which is advantageous in that it comprises only two switches, it would seem that only two of said parameters can be optimized independently. However, in Fig. 2 it is possible to change the switching frequency of both switches together *and* the dutycycle of each switch individually. The average dutycycle controls the mains current, while the dutycycle difference controls the voltage balance on the 10 two buffer capacitors 133, 134 can be changed. This voltage balance influences the necessary duty cycle and frequency of the lower switch, thus indirectly influencing the maximum power point tracking.

It should be clear to a person skilled in the art that the present invention is not 15 limited to the exemplary embodiments discussed above, but that several variations and modifications are possible within the protective scope of the invention as defined in the appending claims.

For instance, parallel to the series arrangement of lamp L, capacitor 135 and inductor 136, or instead of this series arrangement, a transformer driving a rectifier can be 20 connected, so that the driver 100, 200 according to the present invention can also be provided with a battery charger output.